

P8888a

PATENT

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant(s): Masahiko Mizuta, et al. Group Art Unit: 2811
Serial No.: 10/649,153 Examiner: Not Yet Assigned
Filed: August 27, 2003
Title: SEMICONDUCTOR DEVICE AND IN-CIRCUIT EMULATOR USING THE
SAME

CERTIFICATE OF MAILING

I hereby certify that this correspondence and the documents attached hereto, are, on this date, being deposited with the United States Postal Service with sufficient postage as "First Class" mail in an envelope addressed to the Commissioner for Patents, P.O. Box 1450, Alexandria, VA 22313-1450.

Date: January 14, 2004


Ann E. George

REQUEST FOR CORRECTED FILING RECEIPT

Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

Sir:


In connection with the above-identified patent application, an error has been noted in the filing receipt with respect to the following data:

Applicant(s):

Please change "Sappovo-shi" to --**Sapporo-shi**--.

Applicant(s) respectfully requests that a corrected filing receipt be issued and a correction be made of record in the application.

Respectfully submitted,



Mark P. Watson
Registration No. 31,448

Please address all correspondence to:
Epson Research and Development, Inc.
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Date: January 14, 2004



UNITED STATES PATENT AND TRADEMARK OFFICE

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APPL NO.	FILING OR 371 (c) DATE	ART UNIT	FIL FEE REC'D	ATTY. DOCKET NO	DRAWINGS	TOT CLMS	IND CLMS
10/649,153	08/27/2003	2811	880	P8388a	9	11	1

CONFIRMATION NO. 5017

20178
 EPSON RESEARCH AND DEVELOPMENT INC
 INTELLECTUAL PROPERTY DEPT
 150 RIVER OAKS PARKWAY, SUITE 225
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UPDATED FILING RECEIPT



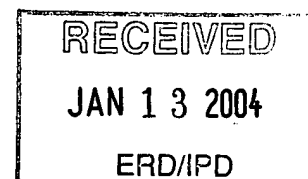
OC000000011634953

Date Mailed: 01/07/2004

Receipt is acknowledged of this regular Patent Application. It will be considered in its order and you will be notified as to the results of the examination. Be sure to provide the U.S. APPLICATION NUMBER, FILING DATE, NAME OF APPLICANT, and TITLE OF INVENTION when inquiring about this application. Fees transmitted by check or draft are subject to collection. Please verify the accuracy of the data presented on this receipt. If an error is noted on this Filing Receipt, please write to the Office of Initial Patent Examination's Filing Receipt Corrections, facsimile number 703-746-9195. Please provide a copy of this Filing Receipt with the changes noted thereon. If you received a "Notice to File Missing Parts" for this application, please submit any corrections to this Filing Receipt with your reply to the Notice. When the USPTO processes the reply to the Notice, the USPTO will generate another Filing Receipt incorporating the requested corrections (if appropriate).

Applicant(s)

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Domestic Priority data as claimed by applicant

Foreign Applications

JAPAN 2002-246228 08/27/2002

If Required, Foreign Filing License Granted: 11/18/2003

Projected Publication Date: 04/15/2004

Non-Publication Request: No

Early Publication Request: No

Title

Semiconductor device and in-circuit emulator using the same

Preliminary Class

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Title 35, United States Code, Section 184
Title 37, Code of Federal Regulations, 5.11 & 5.15**

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